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Attorney's Docket No.: 10559-595001/P12879

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : William R. Wheeler et al. Art Unit: 2825
Serial No.: 09/942,102 Examiner: Annette M.
Filed : August 29, 2001 Thompson
Assignee : Intel Corporation
Title : GENERATING A LOGIC DESIGN

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

Copies of the U.S. patents and publications are not being provided per the waiver posted on the PTO website.

Kindly accept this Information Disclosure Statement under Rule 97(c) (2). The rule 17(p) certification fee of \$180 is enclosed.

The documents are in the English language; hence no concise explanation is necessary per Rule 98(a) (3).

03/30/2004 FFAHAEIA 00000025 09942102

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Applicant : William R. Wheeler et al.
Serial No.: 09/942,102
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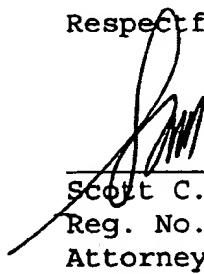
Attorney's Docket No.:10559-595001

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 3/24/04


Scott C. Harris
Reg. No. 32,030
Attorney for Intel Corporation

Fish & Richardson P.C.
USPTO Customer No. 20985
12390 El Camino Real
San Diego, CA 92130
Telephone: (858) 678-5070
Facsimile: (858) 678-5099

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Substitute Form PTO-1449 (Modifed)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-595001	Application No. 09/942,102
Information Disclosure Statement by Applicant <small>(Use several sheets if necessary)</small> <small>(37 CFR §1.98(b))</small>		Applicant William R. Wheeler et al.	
		Filing Date August 29, 2001	Group Art Unit 2825

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(37 CFR §1.98(b))

PATENT & TRADEMARK OFFICE

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	RE 38,059	04/2003	Yano, et al.			
	AB	4,703,435	10/1987	Darringer, et al.			
	AC	4,970,664	11/1990	Kaiser, et al.			
	AD	5,212,650	05/1993	Hooper, et al.			
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	AF	5,287,289	02/1994	Kageyama, et al.			
	AG	5,297,053	03/1994	Pease, et al.			
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	AI	5,493,507	02/1996	Shinde, et al.			
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	AL	5,603,015	02/1997	Kurosawa, et al.			
	AM	5,604,894	02/1997	Pickens, et al.			
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	AAA	6,208,954	03/2001	Houtchens			

Examiner Signature

Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute Form PTO-1449 <i>(Amendment)</i> MAR 29 2004 (37 CFR §1.98(e))		U.S. Department of Commerce Patent and Trademark Office		Attorney's Docket No. 10559-595001	Application No. 09/942,102
Information Disclosure Statement by Applicant <i>(Use several sheets if necessary)</i>		Applicant William R. Wheeler et al.			
		Filing Date August 29, 2001	Group Art Unit 2825		

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	ABB	6,216,256	04/2001	Inoue, et al.			
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	AOO	6,473,885	10/2002	Wallace			
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	AYY	2002/0059054	05/2002	Bade, et al.			
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	ABBB	2003/0004699	01/2003	Choi, et al.			

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Substitute Form PTO-1449 <i>(OTPE)</i> Information Disclosure Statement by Applicant MAR 29 2004 (37 CFR §1.98(e))		U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-595001	Application No. 09/942,102
		Applicant William R. Wheeler et al.		
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U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	ACCC	2003/0036871	02/2003	Fuller, et al.			
	ADDD	2003/0177455	09/2003	Kaufman, et al.			

Foreign Patent Documents or Published Foreign Patent Applications							
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation Yes No
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	AFFF	0 720 233	07/1996	EPO			
*	AGGG	0 901 088	03/1999	EPO			
	AHHH	58-060559	04/1983	Japan			
	AIII	03-225523	10/1991	Japan			
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	AKKK	08-314892	11/1996	Japan			
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	AMMM	WO 98/55879	12/1998	WIPO			
*	ANNN	WO 99/39268	08/1999	WIPO			
*	AOOO	WO 00/65492	11/2000	WIPO			

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Examiner Initial	Desig. ID	Document
	APPB	Gassenfeit, E. H., "Control System Design Realization via VHDL-A: Requirements", Proceedings of the 1996 IEEE International Symposium on Computer-Aided Control System Design, September 15, 1996, pp. 282-285.
	AQQQ	Kutzschebauch, "Efficient logic optimization using regularity extraction", Proceedings of 2000 International Conference on Computer Design, September 17, 2000, pp. 487-493.
	ARRR	Lahti, et al., "SADE: a Graphical Tool for VHDL-Based System Analysis", 1991 IEEE International Conference on Computer-Aided Design, November 11, 1991, pp. 262-265.
	ASSS	Lin, et al., "A Goal Tree Based High-Level Test Planning System for DSP Real Number Models", 1998 Proceedings of International Test Conference, October 18, 1998, pp. 1000-1009.
	ATTT	Maxfield, C., "Digital Logic Simulation: Event-Driven, Cycle-Based, and Home-Brewed", <i>Electrical Design News</i> , 41(14):129-136 (1996).
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Substitute Form PTO-1449 (Mod. 01-02)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-595001	Application No. 09/942,102
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Other Documents (Include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AVVV	NN7807629, "Functional Oriented Symbolic Macromodeling Algorithm", IBM Technical Disclosure Bulletin, Vol. 21, No. 2, July 1978, pp. 629-631.
	AWWW	NN8006341, "Macro Physical-To-Logical Checking LSI Chip Design", IBM Technical Disclosure Bulletin, Vol. 23, No. 1, June 1980, pp. 341-345.
	AXXX	NN9407481, "Functional Modeling using object Collaboration Diagram", IBM Technical Disclosure Bulletin, Vol. 37, No. 7, July 1994, pp. 481-486.
	AYYY	Parlakbilek, et al., "A Multiple-Strength Multiple-Delay Compiled-Code Logic Simulator", <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 12(12):1937-1946 (1993).
	AZZZ	Yli-Pietila, et al., "The Design and Simulation of Complex Multitechnology Systems", IEEE International Conference on Systems Engineering, August 9, 1990, pp. 474-477.

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